

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

*AMENDMENT*

Applicant:	Darsow, <i>et al.</i>	Docket No.:	ROC920030338US1
Serial No.:	10/777,261	Group Art Unit:	2825
Filed:	02/12/04	Examiner:	Dinh, Paul
TITLE:	APPARATUS AND METHOD FOR PERFORMING STATIC TIMING ANALYSIS OF AN INTEGRATED CIRCUIT DESIGN USING DUMMY EDGE MODELING		

Mail Stop AMENDMENT  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

In response to the office action dated 08/18/2006, please amend the subject patent application as follows.

**IN THE SPECIFICATION**

Please delete the paragraph at p. 1 lines 5-8 under the heading “Related Application” and substitute therefor the following replacement paragraph:

This patent application is related to a US patent application “APPARATUS AND METHOD FOR PERFORMING STATIC TIMING ANALYSIS OF AN INTEGRATED CIRCUIT DESIGN”, serial no. \_\_\_\_\_ 10/777,262 filed on \_\_\_\_\_ 02/12/04, which is incorporated herein by reference.